UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/703,181	10/30/2000	Michael T. Moore	CY-0016	9600

7590   05/23/2003

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EXAMINER
LIU, ANDREA

ART UNIT	PAPER NUMBER
2825	

DATE MAILED: 05/23/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

PTO-90C (Rev. 07-01)

-- *The MAILING DATE of this communication appears on the cover sheet with the correspondence address* --

Period for Reply

   A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE *3* MONTH(S) FROM
   THE MAILING DATE OF THIS COMMUNICATION.
   - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed
     after SIX (6) MONTHS from the mailing date of this communication.
   - If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
   - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
   - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
   - Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any
     earned patent term adjustment. See 37 CFR 1.704(b).

Status

1) ☒ Responsive to communication(s) filed on *30 October 2000 - 3 March 2003* .
2a) ☐ This action is **FINAL**.   2b) ☒ This action is non-final.
3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is
   closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4) ☒ Claim(s) *1-23* is/are pending in the application.

   4a) Of the above claim(s) _____ is/are withdrawn from consideration.

5) ☐ Claim(s) _____ is/are allowed.
6) ☒ Claim(s) *1-23* is/are rejected.
7) ☐ Claim(s) _____ is/are objected to.
8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9) ☐ The specification is objected to by the Examiner.
10) ☒ The drawing(s) filed on *30 October 2000* is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.

   Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

11) ☐ The proposed drawing correction filed on _____ is: a) ☐ approved b) ☐ disapproved by the Examiner.

   If approved, corrected drawings are required in reply to this Office action.

12) ☐ The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. §§ 119 and 120

13) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

   a) ☐ All b) ☐ Some * c) ☐ None of:

   1. ☐ Certified copies of the priority documents have been received.
   2. ☐ Certified copies of the priority documents have been received in Application No. _____ .
   3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage
      application from the International Bureau (PCT Rule 17.2(a)).
   * See the attached detailed Office action for a list of the certified copies not received.

14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).

   a) ☐ The translation of the foreign language provisional application has been received.

15) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

Attachment(s)

1) ☒ Notice of References Cited (PTO-892)
2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3) ☐ Information Disclosure Statement(s) (PTO-1449) Paper No(s) _____ .
4) ☐ Interview Summary (PTO-413) Paper No(s). _____ .
5) ☐ Notice of Informal Patent Application (PTO-152)
6) ☐ Other: .

DETAILED ACTION

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the

basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign
country or in public use or on sale in this country, more than one year prior to the date of
application for patent in the United States.

1. Claims 1-20 are rejected under 35 U.S.C. 102(b) as being anticipated by Butts et al.

U.S. Patent No. 5,812,414.

As per claim 1, Butts et al. disclose a programmable portion comprising a plurality of

circuits configurable by a user of the integrated circuit device (columns 7 and 8); and a

communication portion with at least one circuit block to perform conversion of data values

(columns 76-78).

As per claim 2, Butts et al. disclose a programmable portion comprising a programmable

interconnect portion and a logic gate portion (column 8).

As per claim 3, Butts et al. disclose a memory circuit for storing configuration

information for configuring circuits of the programmable portion (columns 23-25).

As per claim 4, Butts et al. disclose a timing circuit that receives a clock signal and

generates an internal clock signal (columns 67-69).

As per claim 5, Butts et al. disclose a plurality of input/outputs commonly connected to

the programmable portion and the communication portion (column 76).

As per claim 6, Butts et al. disclose the communication portion to include a plurality of data operation circuits, each to perform a different function on received input data (columns 76 and 77).

As per claim 7, Butts et al. disclose the data operation circuits to include a block converter circuit (columns 45 and 46).

As per claim 8, Butts et al. disclose data operation circuits with a scrambler circuit (columns 32 and 33).

As per claims 9-12, Butts et al. disclose the communication portion to further include an operation control store with a scrambler circuit, circuits that provide user operational value configured by user, and a data multiplexer (columns 32 and 33).

As per claim 13, Butts et al. disclose the communication portion to include a physical layer circuit that provides a data output stream compatible with a particular data transmission media (column 82).

As per claim 14, Butts et al. disclose a communication portion including a plurality of communication portions (columns 32 and 33).

As per claims 15-20, Butts et al. disclose a programmable logic device having a communication portion embedded therein, the latter also including a plurality of circuit blocks, a selectable data path between each circuit block and a data output, a block converter circuit, and a scrambler circuit (columns 32, 33, 76, and 77).

2. Claims 21-23 are rejected under 35 U.S.C. 102(b) as being anticipated by Roush U.S. Patent No. 5,457,786.

As per claim 21, Roush discloses a method comprising the steps of performing predetermined logic functions on a programmable logic portion of the integrated circuit (columns 1 and 2); and performing serial data communication functions on a communication portion of the integrated circuit that includes circuit blocks that are not formed with programmable logic device configuration data (columns 2-4).

As per claim 22, Roush discloses the method of performing serial data communication functions that includes selecting a polynomial value from a number of polynomial values (columns 3, 7, and 8); and scrambling serial data according to the selected polynomial value (columns 3, 7, and 8).

As per claim 23, Roush discloses the method of performing serial data communication functions that includes encoding serial data having words of different bit length (columns 3, 7, and 8).

Response to Applicant's Comments

In reviewing the prior art sent to the Applicant on 3 December 2002 the Applicant's arguments filed on 3 March 2003 have been considered and found to be persuasive by the Examiner. However, claims 1-23 have been rejected in light of the new prior art enclosed hereto.
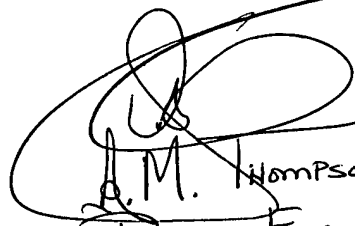
Since the above change was not necessitated by the Applicant's amendment, this is not a Final Rejection.

Status of Application

- Rejection: Claims 1-23

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Andrea Liu whose telephone number is (703) 305-4041.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 1-800/PTO-9199.

A.M. Thompson
PATENT EXAMINER
TECHNOLOGY CENTER 2800